

AMENDMENTS TO THE SPECIFICATION

Beginning on page 5, line 3, please amend the paragraph as follows:

Figure 1 illustrates generalized virtual-to-physical-address translation employed in a wide variety of modern computer systems, and implemented in a wide variety of currently available computer operating systems.

Figure 2 illustrates allocation of five virtual memory pages on behalf of a process.

Figure 3 shows a logical mechanism embodied in processor logic for implementing uninstantiated virtual memory pages according to one embodiment.

Figure 4 illustrates an extension of immediate virtual memory to provide an arbitrary number of different default initializations according to one embodiment.

Figure 5 illustrates logical-circuit components within a processor needed to implement immediate virtual memory according to one embodiment.

~~Figure 5 illustrates logical-circuit components within a processor needed to implement immediate virtual memory according to one embodiment.~~